All-Optical Basic Arithmetic Elements using Dark-Bright Solitons Conversion Control

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Abstract

In this paper, we propose a new design for all-optical basic arithmetic elements using optical technique namely dark-bright solitons conversion control within an optical add-drop filter. In operation, all-optical data input logic ‘0’, ‘1’ is formed by dark soliton (D) and bright soliton (B) pulses, respectively. The conversion behaviour between dark and bright soliton pulses can be obtained and formed the logic pulses by a π/2 phase shifted device such as an optical coupler, in which the binary logic operation can be formed simultaneously at the thought and drop ports, respectively. In all-optical NAND gate and S-R flip-flop application can be recognized as a simple and flexible system for forming the logic switching system. Numerical simulation results are obtained and confirmed for the useful application, in which the advanced logical system can be operated.

Keywords: all-optical, Digital optics, Add-drop filter.

1. Introduction

Near future, computation and communication problems are inevitable since conventional electronic technology will be soon reaching its speed limit. All optical logic gates are the important key element in next generation optical computing and in networking to perform optical signal processing such as binary addition, header reorganization, parity checking, optical bit pattern recognition addressing, demultiplexing, regenerating and switching with very high speed. Thus, the use of light signals has been proposed for signal processing instead of electrons because the electric current flows at only 10 percent of the speed of light. By using light, the optical components for digital processing are required to fulfill the full applications. Till date, the practical components and optical signal processing are still some years away. The optical techniques have been widely increasing and applying for many optical applications for communication use. One of those applications is a processing system. An all-optical flip-flop is the packet switches in the next generation of communication network. The flip-flop is a logic gates that maintain a stable state output even after the input is turned off, in which the key basic function is represented by the logic gates. Nowadays, All-optical flip-flop is an essential component for latching functions in ultrahigh speed all-optical processing applications\textsuperscript{(1,2)}. Currently, many systems such as a terahertz optical asymmetric de-multiplex (TOAD)\textsuperscript{(3)}, semiconductor optical amplifier\textsuperscript{(4)}, Fabry-Perot laser diode\textsuperscript{(5)}, ring lasers\textsuperscript{(6)}, photonic crystals\textsuperscript{(7,8)}, DBR laser diode\textsuperscript{(9)} have been proposed. In this paper, we have presented an all-optical flip-flop system for logical processing operation and arithmetic operation base tree architecture, which can be used for an electronic circuit replacement, which can be replaced used for an electronic circuit\textsuperscript{(10-13)} by using dark-bright soliton pulses conversion control\textsuperscript{(14,15)}, the theoretical background is also reviewed. For our concept, the all-optical flip-flop operations and arithmetic operations of binary based on dark-bright solitons conversion behaviors can be performed, in which the coincidence dark and bright soliton pulses can be separated after propagating into the optical-filter device with π/2 phase shifted (an optical coupler)\textsuperscript{(14)}. The proposed scheme is based on a 1 bit binary to the complex logic circuits, which can be
compared by any 2 bits, when logic ‘0’ and ‘1’ are formed by dark and bright soliton pulses, respectively.

In this paper, we propose an all-optical circuit for logic NAND Gate, S-R flip-flop and arithmetic operations. The theoretical background is also reviewed. In this concept, the simultaneous logic operation of binary based on dark-bright soliton conversion behaviors can be performed, in which the coincidence dark and bright soliton pulses can be separated after propagating into the π/2 phase shifted device (an optical coupler). The proposed scheme is based on a 1 bit binary compared to the complex logic circuits, which can be compared by any 2 bits, when logic ‘0’ and ‘1’ use the dark and bright soliton pulses, respectively.

2. Dark-Bright Solitons Conversion Control

The operation of dark-bright solitons conversion\(^{(16)}\) using a ring resonator as shown in Figure 1, where in this case the dark-bright solitons conversion system using an optical channel dropping filter (OCDF) is composed of two set of coupled waveguide, as shown in Figure 1 (a) and (b), when for convenience, Figure 1 (b) is replaced by Figure 1 (a). The relative phase of the two output light signals after coupling into the optical coupler is π/2 before coupling into the ring and the input bus, respectively. This means that the signals coupled into the drop and through ports are acquired a phase of π with respect to the input port signal. In application, if the coupling coefficients are formed appropriately, the field coupled into the through port would completely extinguish the resonant wavelength, and all power would be coupled into the drop port, in which the dark-bright conversion behaviors are described by Eqs. (1) to (8).

\[
E_{ia} = -jK_iE_i + \tau_1E_{id} \quad (1)
\]

\[
E_{ib} = \exp(j\omega T/2)\exp(-\alpha L/4)E_{ia} \quad (2)
\]

\[
E_{ic} = \tau_2E_{ib} + jK_iE_{ia} \quad (3)
\]

\[
E_{id} = \exp(j\omega T/2)\exp(-\alpha L/4)E_{ic} \quad (4)
\]

\[
E_i = \tau_1E_i - jK_iE_{id} \quad (5)
\]

\[
E_d = \tau_2E_d - jK_iE_{ib} \quad (6)
\]

where \(E_i\) is the input field, \(E_a\) is the added (control) field, \(E_t\) is the throughput field, \(E_d\) is the dropped field, \(E_{ia} \ldots E_{id}\) are the fields in the ring at the point \(a \ldots d\). \(K_i\) is the field coupling coefficient between the input and the ring, \(K_2\) is the field coupling coefficient between the ring and the output bus, \(L\) is the circumference of the ring (2\(\pi R\)), \(T\) is the time taken for one round trip, and \(\alpha\) is the power loss in the ring per unit length. We assume that lossless coupling, i.e. \(\tau_{12} = \sqrt{1 - \kappa_1^2}\), \(T\) is the round-trip time \(T = Ln_{eff}/c\). The output power-intensities at the drop port and through port are given by

\[
|E_a|^2 = \left| \frac{-\kappa_1^2 A_{1/2} \Phi_{1/2} \tau_1 - \tau_1 A \Phi - \tau_1 A \Phi E_a}{1 - \tau_1 A \Phi} \right|^2 \quad (7)
\]

\[
|E_d|^2 = \left| \frac{\tau_2 - \tau_1 A \Phi - \tau_1 A \Phi E_a}{1 - \tau_1 A \Phi} \right|^2 \quad (8)
\]

where \(A_{1/2} = \exp(-\alpha L/4)\) (the half-round-trip amplitude). \(A = A_{1/2} \cdot \Phi_{1/2} = \exp(j\omega T/2)\ldots\) (the half-round-trip phase contribution), and \(\Phi = \Phi^*\). The input and control fields at the input and add ports are formed by the dark-bright optical solitons as shown in Eqs. (9) – (10).

\[
E_a(t) = A_0 \sec h \left[ \frac{T}{T_0} \exp \left( \frac{z}{2L_0} \right) - i\alpha t \right] \quad (9)
\]

\[
E_d(t) = A_0 \tanh \left[ \frac{T}{T_0} \exp \left( \frac{z}{2L_0} \right) - i\alpha t \right] \quad (10)
\]

where \(A\) and \(z\) are optical field amplitude and propagation distance, respectively. \(T\) is soliton pulse propagation time in a frame moving at the group velocity \(T = t - \beta_2 c\), where \(\beta_1\) and \(\beta_2\) are the coefficients of the linear and second-order terms of Taylor expansion of the propagation constant. \(L_0 = T_0^2/|\beta_2|\) is the dispersion length of the soliton pulse. \(T_0\) in the equation is the initial soliton pulse width, where \(t\) is the soliton phase shift time, and the frequency shift of the soliton is \(\omega_0\). This solution describes a pulse that keeps its temporal width invariance as it propagates, and thus is called a temporal soliton. When a soliton peak intensity is given, then \(T_0\) is known. For the soliton pulse in the nanoring device, a balance should be achieved between the dispersion length \(L_0\) and nonlinear length \(L_{NL} = 1/|\beta_2|\), where \(\Gamma = n2k\theta\) is the length scale over which dispersive or nonlinear effects make the beam become wider or narrower. For a soliton pulse, there is a balance between dispersion and nonlinear lengths, hence \(L_0 = L_{NL}\).

When light propagates within the nonlinear material (medium), the refractive index \((n)\) of light within the medium is given by Eq. (11)

\[
n = n_0 + n_1 I = n_0 + \frac{n_2}{A_{eff}} P \quad (11)
\]
here $n_0$ and $n_2$ are the linear and nonlinear refractive indexes, respectively. $I$ and $P$ are the optical intensity and optical power, respectively. The effective mode core area of the device is given by $A_{\text{eff}}$. For the micro/nano ring resonator, the effective mode core areas range from 0.10 to 0.50 $\mu m^2$. The resonant output of the light field is the ratio between the output and input fields $[E_r$ and $E_i]$ in each round trip, which is given by references16).

![Figure 1. A schematic diagram of the dark-bright soliton conversion control system](image1)

3. All Optical Logic NAND Gate

The proposed all-optical logic NAND gates devise is as shown in Figure. 2. The input and control light pulse trains are input in to the first add/drop filter (No. “01”) using the the dark-bright solitons, where firstly, the dark soliton is converted to be dark and bright solitons via add/drop filter, which can be seen at the through and drop ports with $\pi$ phase shift, respectively. By using add/drop filter (No. “11”, “12”), both input signals are generated by the first stage add/drop filter. Next, the input data “A” with logic “0” (dark soliton) and logic “1” (bright soliton) are added into both add ports. Secondly, the dark-bright soliton conversion with $\pi$ phase shift is operated again. Finally, by using add/drop filter (No. “21” and “22”), the input data “B” with logic “0” (dark soliton) and logic “1” (bright soliton) are added at all the add ports. For large scale of circuit shown in Figure. 2, results obtained are simultaneously seen by D21, D22, T21, T22 at the drop and through ports for optical logic gates, respectively. The truth table of logic NAND gate is as shown in Figure. 3.

![Figure 2. Schematic diagram simultaneous NAND gates](image2)

<table>
<thead>
<tr>
<th>Input A</th>
<th>Input B</th>
<th>Output</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

![Figure 3. Truth table of logic NAND gate](image3)

In simulation, add/drop filter fixed parameters used are $R_{ad}=1.50\mu m$, $A_{ad}=0.25\mu m^2$, $a=0.05\mu m$, the couple intensity loss is $\gamma=0.1$, $n_0=3.34(\text{InGaAsP/InP})$, the coupling coefficient of the micro-ring resonator is $k_r=0.5$.

The nonlinear refractive index is $n_2=2.2x10^{-17}$ with center wavelength of $1.5 \mu m$ and pulse width $20ns$, power at 1 W is input into add/drop system.

Results of the NAND gate are generated by the dark-bright soliton conversion behaviors, the input data logic “0” and “1” are represented by the dark and bright soliton pulses, respectively. However, in practice, the time domain results can be obtained by using the term $T=1/f$, where $T$ and $f$ are a period and frequency of the signal. In Figure. 4. Figure. 5. Figure. 6 and Figure. 7.

Case 1: When the input data logic “00” is added, the obtained output optical logic is “1000” (Figure. 4).

Case 2: When the input data logic “01” is added, the output optical logic “1010” (Figure. 5) is obtained.

Case 3: When the input data logic “10” is added, the output optical logic “1100” (Figure. 6) is obtained.

Case 4: When the input data logic “11” is added, we found that the output optical logic “0110” (Figure. 7) is seen.

From the simulation result, we found that output data logic at the through port T22 is optical NAND gate, the output data logic at drop port D21 and through port T22 are same with the input “A” and “B”, respectively.
Figure 4. Show the output logic gate when the input logic stages ‘00’ or ‘DD’.

Figure 5. Show the output logic gate when the input logic stages ‘01’ or ‘DB’.

Figure 6. Show the output logic gate when the input logic stages ‘10’ or ‘BD’.

Figure 7. Show the output logic gate when the input logic stages ‘11’ or ‘BB’.

4. All Optical S-R Flip-Flop

S-R flip-flop is a circuit with two cross-coupled NOR gates or two cross-coupled NAND gate. It has two input signals, which one is S and the other is R for setting and resetting the outputs $Q_{n+1}$ and $\bar{Q}_{n+1}$, respectively. The S-R flip-flop outputs are the feedback signals. There are three status logic output signals, which is called “Hold” when no input signal ($S = 0$, $R = 0$), “Set” when $S = 1$ and $R = 0$ and “Reset” when $S = 0$, $R = 1$, respectively. The proposed scheme for all-optical S-R flip-flop using beam splitter (BS) and beam combiner (BC) is as shown in Figure 8. The input logic ‘1’ is formed by bright soliton and logic ‘0’ is formed by absence of the input signal. The other technique that can be used to form the similar operation is found in reference (12), in which the optical power signal is manipulated by erbium doped fiber amplifier (EDFA).

In simulation, the add/drop optical filter parameters are used and fixed to be $K_s = 0.5$, $R_d = 1.55 \mu m$, $A_{eff} = 0.25 \mu m^2$, $\alpha = 0.05 dBm^{-1}$, $\gamma = 0.01$, $n_{eff} = 3.14$ (for InGaAsP/InP) for all add/drop optical filters in the system. Results of the all-optical flip-flop are generated by the dark-bright solitons conversion behaviors, with center wavelength is at $\lambda_0 = 1.50 \mu m$, pulse width of 35fs and the input data logic “0” and “1” are represented by the dark and bright soliton pulses, respectively. In Figure 9, the simultaneous output optical logic is seen, which can be configured as following details.

Case 1: When it is no input signal, $S = R = 0$, where $Q_{n+1}$ and $\bar{Q}_{n+1}$ are not changed. This state is called “Hold”.

Case 2: By using the dark-bright solitons conversion behavior, when $S = 0$ and $R = 1$, the input logic ‘0’ and ‘1’ is formed by dark-bright solitons, where both input signals are
converted to dark-bright soliton pulses with \( \pi \) phase shift. The detector D2 receives the logic ‘0’ (dark soliton), i.e. \( Q_{e1} = 0 \), which is connected to the input ‘S’, then the dark-bright solitons conversion with \( \pi \) phase shift is operated again. When the detector D1 receives the logic ‘1’ (bright soliton), hence \( \overline{Q}_{e1} = 1 \), in which \( Q_{e1} \) and \( \overline{Q}_{e1} \) are connected to the input ‘S’ and ‘R’, respectively. Therefore, if both ‘S’ and ‘R’ are no input signals, i.e. \( S = R = 0 \), then \( Q_{e1} \) and \( \overline{Q}_{e1} \) are the feedback signals, which is called the “Reset”.

Case 3: When \( S = 1 \) and \( R = 0 \), the logic ‘0’ and ‘1’ is formed by dark-bright solitons, where both input signals are converted to dark-bright solitons with \( \pi \) phase shift. When D1 receives the logic ‘0’ (dark soliton) i.e. \( \overline{Q}_{e1} = 0 \), which is connected to the input ‘R’, then the dark-bright solitons conversion with \( \pi \) phase shift is formed. When the detector D2 receives the logic ‘1’ (bright soliton), hence \( Q_{e1} = 1 \), where \( Q_{e1} \) and \( \overline{Q}_{e1} \) are connected to the input ‘S’ and ‘R’, respectively. Therefore, if both ‘S’ and ‘R’ are no input signals, i.e. \( S = R = 0 \), then \( Q_{e1} \) and \( \overline{Q}_{e1} \) are the feedback signals, which is called the “Reset”. (See Figure 10)

Case 4: When \( S = R = 1 \) then both MRR1 and MRR2 receive both the incoming bright soliton signals, i.e.

\[ Q_{e1} = \overline{Q}_{e1}, \]

this state is called the “Forbidden”. (See Figure 11)

The truth table for output of the one bit binary R-S Flip-Flop is concluded in Table: 1.

<table>
<thead>
<tr>
<th>Input</th>
<th>Output</th>
<th>State</th>
</tr>
</thead>
<tbody>
<tr>
<td>S</td>
<td>R</td>
<td>( Q_{e1} )</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

Logic ‘0’ = ‘Dark soliton, Logic ‘1’ = ‘Bright soliton’

5. Simultaneous All-Optical Half Adder/Subtractor

A binary arithmetic is performed similarly to the decimal arithmetic, which is presented by the logic gate operation, which is as shown in Figure 12. (a). For simplicity, the multiple input ports are required to perform the operation, where first of all, the required half adder/subtractor truth table is given and shown in Figure 12. (b). For the half adder/subtractor with two binary inputs, the simplified Boolean equation is obtained, in which is the sum of product for half adder and half subtractor, respectively. The simplified output of Sum and Difference can be also implemented with the XOR gate, in which the addition and subtraction operations can be combined into one circuit with one common binary adder. To generate the all-optical half adder/subtractor, it can be easily done by using a beam splitter (BS) or beam combiner (BC), as shown in Figure 3, the same beam splitter is also used in this case.
We proposed photonic circuit can be used to create the logic NAND gate and a new designed for basic all-optical flip-flop and arithmetic operations based on dark-bright solitons conversion control system via the modified add/drop filters for ultrahigh speed memory or signal processing application. Based on the dark-bright solitons conversion control concept, the data logic ‘0’ (dark soliton) and ‘1’ (bright soliton) using all-optical in nature can be used to form the flip-flop operations, in which the logic status results can be obtained simultaneously at the drop and through ports, respectively. The all-optical device can perform high capacity optical logic NAND at output port the T21. And the proposed design can be used for logical circuit which is recognized as the simple and flexible system for performing the logic switching system and the device can be extended and implemented for any higher number of input digits by a proper incorporation of dark-bright solitons conversion control.

References


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