Discussions on Novel Inverters Constructed by Minimum Components

- Pursuit for simple power conditioner -

Keiju Matsui\textsuperscript{a*}, Eiji Oishi\textsuperscript{a}, Masayoshi Umeno\textsuperscript{a}, Mikio Yasubayashi\textsuperscript{b}, Hideo Uchida\textsuperscript{b}, Masaru Hasegawa\textsuperscript{b}

\textsuperscript{a}Minna-denyoku, Inc. Setagaya Monozukuri Gakko, 210 Setagaya 154-0001, Japan
\textsuperscript{b}Chubu University Kasugai 487-8501, Japan

Abstract
Photovoltaic power generations (PVG) have been accepted and spread widely. Various power conditioning systems used in them have been also studied by many researchers. In general utilization, the actual application of such solar panels are almost installed on top of the roof of isolated house. Some residents living in the apartment house, however, have fairly desires to contribute for energy saving due to natural energy generation. The generating power in such case is fairly reduced, so the system construction should balance the reduced power. Thus, it is necessary to improve the construction toward simple one. In this paper, in order to give a reply, simple and concise power conditioners, especially novel inverter is proposed. Considering fairly reduced generating power and narrow space of installation, the system constructions should be compact. The circuits which gratifies their operating characteristic is presented and analytically discussed about circuit construction as a novel converter.

Keywords: Power Conditioner, PCS, Solar cell, Minimum construction, Buck-boost inverter, Boost inverter, Half bridge, photovoltaic power generation

1. Introduction

It is an important assignment for humanity of the world to conquer the problems against environmental conservation with preserving the developments of economy, science and technology. The usage of the renewable energies is expected to be able to mitigate such problem, which is an important strategies [1,2]. Among them, photovoltaic power generation have various advantages such as usage of inexhaustible and unpolluted sunlight, which is advantageous for maintenances, easily construction from small power to large one, and can be installed in various location including city center. In such a way, this power generation system is a promising one with bright future.

As a utilization form, utility interactive power generation system has been accepted and widely spread. In such system, it is necessary to install the chopper, where the lower voltage of solar cell should be boosted to suitable voltage, and inverter where the dc power of boosted voltage should be converted to ac power in the power system [3,4].

The power conditioners (PCS) including inverter have been presented in various systems so far [5,6]. However, it is necessary to reduce the cost even more. It is said that the system is approaching to an ideal ones with respect to efficiency and construction strategy, but that cost would prevent wide spread if there is no public financial support. In such discussions, there are many subjects to be solved to utilize the PV power in utility interactive power generation. Even more, various safeguard equipment required according to regulations make the cost increase. Thus, it is required to obtain even lower cost PCS. In an extremely reduced power PCS as discussed in this paper, a way of handling would be different compared to typical power range. In such case of reduced generating power, quantities of reversed power to the power system would be small, where another mitigated regulation or deregulation might be approved. Thus, in such PV power generation systems, there are so many subjects to be resolved [7].

Under such circumstances accepted by electrical utility industry as recognitions and assessments for renewable energy, spread of application is strongly sustained by financial supports of public organizations, and a lot of consumers are hoping to install such PV power generation system. For ordinary homes, installation of solar panel is restricted to house having roofs. However, a lot of residents living in the apartment house also wish to install the PV panel under considering resource conservation with such public financial support. In the present situation, however, there is no scheme to perform these requirements. The authors have been studied in a series of the small power PV system [8,9]. In this paper, some simple PCS systems, especially the components like inverters and chopper are integrated toward simple construction, which are presented and discussed.
2. Conception and its Object of Unified BC-INV

Summary of circuit configuration [10]

Under a few hundred watts of small photovoltaic power generation system (PGS), it is important to pursue for simple circuit configuration in proportion to small power. In order to realize this situation, the unified chopper and inverter circuit configuration came to an idea to the authors. In this strategy, one of the conventional inverter switches was also used for the switch of boosting chopper, which brings a simple and low cost configuration.

![Fig.1. Previously presented PCS constructed by half bridge[10].](image1)

The remarkable characteristic is to obtain the boosted voltage inverter function, by means of merely changing the connecting point of the input power supply from the dc link terminal which is the midpoint of inverter leg. By such a way, simple BC-INV can be obtained in a very simple manner. By means of this, the dc link voltage becomes double and an unique sinusoidal inverter can be obtained in half bridge constructed by the minimum circuit components.

However, the applied voltage across the chalk inductor \( L_1 \) becomes \( v_{L1} = v_{AB} \), where the commercial frequency is generated in the chalk inductor. Consequently, the size of \( L_1 \) would become much large. Because of such reasons, for practical applications, there is a problem, so in practice, it could be applied for current source inverter and the like, so the application is limited to restricted fields.

3. Further Development to Novel Inverter having Minimum Components

![Fig.2. Simple Construction in Minimum Components](image2)

for case 1.

3.1 First version of novel inverter in minimum components

Fig.2 shows the first version of novel inverter constructed by minimum component. From the fact that the applied voltage across the inductor \( L_1 \) in Fig.1 is the commercial frequency, the idea is arisen if ac load might be installed at this corresponding place.

The relationship of input and output voltage can be obtained as follows; The modulation method is given by “Natural Sampling (NS) method”. Let the modulation index be \( \alpha \), so the ac load voltage becomes

\[
e = aE \cdot \sin \omega t \]

The following relation can be obtained.

(a) for \( e > 0 \)

- \( S_2 \) is turned-on,

\[
E = v_{L1} + e \]

(b) at \( S_2 \) is turned-off,

\[
E + v_{L2} = e + v_C \]

where \( v_{L1} \) and \( v_{L2} \) are considered for their polarities of applied voltage.

In the NS method which has positive and negative polarity triangular waves, each pulse width of the output voltage given by positive and negative waves is varied in \((1+\alpha)\) or \((1-\alpha)\), where one cycle is given by normalized \( T = 2 \). By using “Inductor Equal Area Rule” for positive and negative voltage waveforms,

\[
(E - e)(1 + \alpha) = (e - E + v_C)(1 - \alpha) \]

From this result,

\[
v_C = (2E - 2e)/(1 - \alpha) \]

The value of \( e = aE \), where \( e \) is constant during corresponding period, is substituted, then

\[
v_C = 2E \]

The result can be obtained clearly.

(b) for \( e < 0 \)

- \( S_1 \) is turned-on.

\[
v_C = v_{L1} + e + E \]

- \( S_1 \) is turned-off.

\[
v_{L2} = e + E \]

where \( v_{L1} \) and \( v_{L2} \) are applied voltage across inductor considering their polarities.

In similar as above mentioned,
\[ (v_C - E - e)(1 + a) = (e + E)(1 - a) \]  \hspace{1cm} (9)

Thus,
\[ v_C = (2E + 2e)/(1 + a) \]  \hspace{1cm} (10)

Substituting \( e = aE \) into (10),
\[ v_C = 2E \]  \hspace{1cm} (11)

In this equation, the relationship in voltage ratio between input and output is two times. During period of (b) \( e < 0 \), however, there is a power regenerative period, where power is returned from \( V_C \) to \( E \). In order to improve this phenomenon, the current flowing route is altered.

### 3.2 Second version of novel inverter in minimum component

Fig.3 shows second version of novel inverter in minimum component obtained by modifying Fig.2.

![Fig.3. Simple Construction in Minimum Component for case 2.](image)

In the first version of Fig.2, the voltage of auxiliary double capacitors providing the negative load current, is given by \( V_C = 2E \), which brings the regenerative power flow. In order to avoid this phenomenon, a modified circuit configuration is provided as in Fig.3.

Instead of using the output capacitor \( C_1 \) and \( C_2 \) like in Fig.2, the capacitor \( C_1 \) in Fig.3 is used as auxiliary one which can be easily derived from Fig.2. The operation can be performed as follows;

The modulation method is used in similar to the first version of single triangular wave with positive and negative polarities, that is NS method. The following relationship can be obtained.

(a) for \( e > 0 \)
- during \( S_1 \) turned-on
  \[ E = v_{L2} + e \]  \hspace{1cm} (12)
- during \( S_2 \) turned-off
  \[ v_{L2} = e + V_C \]  \hspace{1cm} (13)
  where \( v_{L2} \) and \( v_{L2} \) are considered for their polarities of applied voltage,

In the NS method, each pulse width of the output voltage is varied in \((1 + \alpha)\) and \((1 - \alpha)\). By using “Inductor Equal Area Rule”,
\[ (E - e)(1 + \alpha) = (E + v_{L2})(1 - \alpha) \]  \hspace{1cm} (14)

From this result, following equation can be obtained.
\[ v_{C2} = (E - e)/(1 - \alpha) \]  \hspace{1cm} (15)
\[ v_C = E \]  \hspace{1cm} (16)

(c) for \( e < 0 \)
- during \( S_1 \) turned-on

\[ v_C = v_{L2} + e \]  \hspace{1cm} (17)
- during \( S_2 \) turned-off

\[ v_{L2} = e + E \]  \hspace{1cm} (18)

In a similar way as above mentioned,
\[ (v_C - e)(1 + \alpha) = (e + E)(1 - \alpha) \]  \hspace{1cm} (19)

Thus,
\[ v_C = (E - aE + 2e)/(1 + a) \]  \hspace{1cm} (20)

In substituting \( e = aE \) into this equation,
\[ v_C = E \]  \hspace{1cm} (21)

The relationship can be obtained clearly that the voltage between input and output becomes equal. The disadvantageous characteristic of output voltage double can be revised, where there is no regenerative operation. Consequently, that capacity of output capacitor can be reduced.

### 3.3 Power delivery flowchart

Fig.4 shows the power flowchart for the operation of circuit shown in Fig.3. It can be seen that is more effective strategy in comparison both circuits. In the figure, the power flows from dc power supply \( E \) through inductor \( L \) and load \( R \) to auxiliary capacitor \( C_1 \). 1-d or 1+d represents normalized conduction period.

![Fig.4. Power Flowchart of Proposed Inverter: input supply E to capacitor C.](image)

(a) Explanation about power flowchart

Let one switching cycle be \( T = 2 \) in normalization. During switch \( S_1 \) turned-on, that is \((1+d)\), the voltage, \( (E - v_B) = E \times (1-d) \) is applied across the \( L \) and provide the magnetic energy, that is, \( E \times (1+d) \times (1-d) \).

During switch \( S_2 \) turning-off, that is \((1-d)\), \( R \) is supplied due to discharged \( L \) and the auxiliary capacitor is supplied as follows;
\[ (1-d) \times E \times (1+d) = (1-d) \times (v_C + v_B) \]  \hspace{1cm} (22)

From these equations, the relationship of \( v_C = E \) can be obtained, where the auxiliary capacitor voltage becomes equal to the dc supply voltage.

(b) Explanation of power transmission in power sharing by inductor

The stored energy \( W_L \) in \( L \) is delivered to the load \( R \), and then to \( C \) at turned-off. The stored energy \( W_L \) in \( L \) is also delivered to \( C \) which is returned to the load again as \( W_B \). Because of this reason, it is required to be given by the relationship of \( W_L \geq W_B \). These equations can be obtained as follows; The delivered
power to the load is $\Delta W_R$ at turned-on, which can be obtained as,

where equation is normalized by $E$ and the last term $(s1 \Delta \frac{1}{2})$ is abbreviated from this equation.

This value is applied to the above mentioned equation. The pulsedwidth is varied according to the signal of $\alpha \sin \theta$. At $\theta = 0$, the pulsedwidth $\Delta T(\theta)$ becomes $\Delta T/2$ as shown like,

$$\Delta T(\theta) = \frac{1}{2} + (1/2) \alpha \sin \theta \quad \cdots \quad (24)$$

During turned-on period, the stored energy in $L$ is

$$W_L = \int [\Delta W_L \cdot \Delta T(\theta) \cdot d\theta]$$

$$= (\pi/2)(1 - \alpha^2/2) \quad \cdots \quad (25)$$

In the next step, the transmission power to $C$ is to be solved. The pulsedwidth during turned-off is

$$\Delta T(\theta) = \frac{1}{2} - (1/2) \alpha \sin \theta \quad \cdots \quad (26)$$

By using the previously mentioned relationship of $\nu_c = E$, this equation is integrated and solved the stored energy $W_C$ in $C$ such as,

$$W_C = \int [\Delta T(\theta) \cdot d\theta]$$

$$= (\pi/2)(1 - 2\alpha^2/\pi) \quad \cdots \quad (27)$$

In reference to (22), the delivered power can be obtained as the mentioned $\Delta W_R$

$$W_R = \int [\Delta W_R \cdot d\theta]$$

$$= 2\alpha \quad \cdots \quad (28)$$

From relation of $W_C \geq W_R$

$$(\pi/2)(1 - 2\alpha^2/\pi) \geq 2\alpha \quad \cdots \quad (29)$$

$$\alpha \leq 0.524 \quad \cdots \quad (30)$$

It can be seen from this equation that in the proposed power transmission mechanism through $L$ and $C$, it is required to be given by the reduced modulation index.

4. Another Development towards High Frequency Inverter

4.1 Power flowchart

Fig.5 shows another circuit configuration having paralleled load in contrast series load in Fig.3. In comparison to the conventional high frequency inverter, where in half bridge circuit using boosted chopper, the obtained high frequency is rectified as dc-dc converter [12], the proposed configuration seems to be the same as the number of the circuit component.

Fundamental construction can be obtained in Fig.1. With regard to the efficiency, however, the power conversion process of the proposed one is simple and the number of conversion stage is reduced, so the proposed circuit is prominent in comparison. Those validities are confirmed as follows;

In the figure, in contrast to above mentioned series load circuit, in parallel load, the sinusoidal wave cannot be obtained. Regarding rectangular wave in high frequency, it is more effective. When the switch $S_2$ is turned-on at the beginning, the power supply provides the load resistor $R$

$$\Delta W_R = \alpha \sin \theta \quad \cdots \quad (23)$$

and inductor $L$ in parallel connection. Secondly, when $S_2$ is turned-off and $S_1$ is turned-on, the stored energy in $L$ is provided to $R$ and at the same time the excess energy is delivered to the auxiliary capacitor. During last half period, as the supplied power from $L$ is reduced gradually, a deficiency for supplied power from $L$ is provided from auxiliary capacitor $C$. Looking at the whole period, it can be seen that the contribution for power transmission through inductor $L$ is predominant. On the other hand, the power transmission through $C$ is a role of auxiliary power transmission. The percentage of power transmission from $L$ to $R$ is about 30% at usual specification, while one from $L$ to $R$ through $C$ is 20%. For your information, the one of the direct transmission $E$ to $R$ is 50%. It is interesting that the auxiliary capacitor $C$ plays a role of like filter.

In Fig.6, the power line is plotted by heavy solid line, where transmitted power is fairly large, while thin line represents small power transmission. Looking at the whole period, it can be seen that the contribution for power transmission consists of direct transmission from $E$ to $R$. In addition, the route through inductor $L$ is also predominant. It is remarkable strategy that the power transmission is constructed by the direct transmission. Furthermore, as another route, one is via-point of $L$ and the other is via-points of $L$ and $C$.

Fig.7 shows the power flow chart for the conventional high frequency inverter reported as BHB-Boost Half Bridge [12], in which the circuit mechanism is compared and discussed with our strategy. The input power supply voltage is boosted in the half bridge inverter. After this operation, the high frequency ac power is obtained. After the energy of input power supply $E$ is stored into $L$, the capacitor $C$ is charged from power from $L$ at switch turned-off. By means of this capacitor charge, the load $R$ is supplied. The figure shows the flowchart of such operating mechanism, where there are three step power transmissions. On the other hand, for the proposed high frequency conversion, main transmission is performed by direct conversion from $E$ to load, the other is a simple transmission way via single $L$ or $C$. The number of conversion stage is much reduced. Consequently, the transmission efficiency can be expected as much improvement.

Fig.5. Proposed Circuit with Parallel Loads.
4.2 Development towards buck inverter

For further development with minimum circuit component, another method can be derived as shown in Fig.8. By means of alternating switching of S₁ and S₂, the power is delivered to the load R. If the inductor L is connected in series to R, the sinusoidal wave can be obtained. In this figure, however, the rectangular wave is kept in mind. Unbalanced charge of C is to be concerned, but it can be seen that balanced charge characteristic is realized by following discussion. In steady state condition, when S₁ is turned on

\[ E - v_c(0) = R i^+ \]

where \( v_c(0) \) is operating voltage of C, \( i^+ \); is charging current, and \( i^- \) is discharging current.

- In steady state condition, as \( i = i^- \)
  \[ v_c(0) = E/2 \], C is charged by half of supply voltage.

- In transient condition,
  when S₁ is turned on,
  \[ E - v_c(t) = RC \cdot \frac{dv_c(t)}{dt} \]
  when S₁ is turned off
  \[ v_c(t) = -RC \cdot \frac{dv_c(t)}{dt} \]

When the gradient of C, \( \frac{dv_c(t)}{dt} \) becomes equal, relationship of \( E = \frac{v_c(t)}{2} \) establishes and C is charged by E/2.

4.3 Flowchart of buck inverter

In Fig.9, at S₁ turned-on and S₂ turned-off, the load R and C are supplied simultaneously. When S₁ is turned off and S₂ turned on, the R is supplied from power of C. The half power from E is directly supplied to load R which contributes to efficiency improvement that is rather advantageous.

Fig.10 shows the flowchart of conventional half bridge inverter for comparison. For half bridge, C is connected in series with supplied power, image of sequential power transmission may be occurred. But actually, the circuit is changed by C₁ and C₂, and power is delivered by two steps.

5. Comparison of Estimated Transmission Efficiency

5.1 Local transmission efficiency

The number of component of circuit construction gives an influence for efficiency. The estimated efficiency will be calculated by means of number of stages by which
goodness of converter will be estimated. In order to estimate the whole power transmission efficiency, efficiency of one transmission is assumed to be, for example, 90%.

(a) The proposed parallel load (Fig.5) Following each percent shows the transmitted power considering the transmission efficiency. Each transmission power is shown as follows;

\[ E \rightarrow R (90\%), E \rightarrow L(90\%), L \rightarrow R (54\%) \rightarrow C(36\%), C \rightarrow R (36\%) \]

The quantities of transmission power are represented by weighing method. Total efficiency is \(84.0\%\).

(b) Buck-Bridge method (Fig.7)

\[ E \rightarrow L(90\%), E \rightarrow C(90\%), L \rightarrow C(90\%) \rightarrow R(90\%) \]

The quantities of transmission power are represented by weighing method. Total efficiency is \(77.0\%\).

The efficiency is about reduced by 10%. For proposed circuit is improved because of direct transmission for half period.

(c) Buck inverter (Fig.8) Weighting transmission efficiency is

\[ E \rightarrow R (90\%), E \rightarrow C(90\%), C \rightarrow R(90\%) \]

The quantities of transmission power are represented by weighing method. Total efficiency is \(85.5\%\).

(d) Conventional half bridge inverter (Fig.10)

\[ E \rightarrow R (90\%), E \rightarrow C_1(90\%), C_1 \rightarrow R(90\%) \]

In the following switching, operation is transferred to the charging of \(C_2\), and the current of \(R\) is switched to the charge of \(C\), and direction of current is also switched as, \( E \rightarrow R (90\%), E \rightarrow C_2(90\%), C_2 \rightarrow R(90\%) \).

The total efficiency is \(85.5\%\).

5.2 Comparison of efficiency

Table 1 Characteristics of Various Converter.

<table>
<thead>
<tr>
<th>Converter</th>
<th>Eff %</th>
<th>Num.</th>
<th>Load V</th>
<th>Num.</th>
<th>Max. Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>Buck-Boost Inv</td>
<td>84.0</td>
<td>8</td>
<td>E</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>Buck-Half-Bridg</td>
<td>77.0</td>
<td>9</td>
<td>E</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>Buck Inverter</td>
<td>85.5</td>
<td>7</td>
<td>E/2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>Half Bridge Inv.</td>
<td>85.5</td>
<td>8</td>
<td>E/2</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

Table 1 represents above mentioned efficiency for comparison. Upper two stages and lower two ones are analogous construction, respectively. Comparing Buck-boost Inverter with BHB, for load voltage, the supply voltage can be applied directly as advantage. The maximum current of component is double of load current as disadvantage. For proposed converter, the number of component is reduced from nine to eight, which brings efficiency improvement. In lower two stages, the load voltage is reduced to half of the supply voltage as disadvantage, but the load current does not flow over load current. Comparing both inverters, the number of component of Buck Inverter is reduced by unity.

6. Conclusions

The paper is proposed and presented whose idea is obtained from unified inverter circuit constructed by chopper and inverter circuit. In the first version which can realize sinusoidal wave, the number of the circuit construction can be made minimum. The number of the conventional corresponding circuit configuration is totally ten, while for the proposed construction, the number is eight. This result is the reason why the proposed inverter is called as minimum circuit construction. In parallel load method, which can be developed to dc to dc converter, as the number of conversion stages is reduced satisfactorily, an improved efficiency can be achieved. Finally as looking at the whole view compared with the usual half bridge construction, one of double supply capacitors is replaced by dc power supply from regular position, that is an interesting appearance.

Acknowledgements

This research is mostly supported by a grant of the NEDO (New Energy and Industry Technology Development Organization). We would like to express our appreciation to who it may concern about this project.

References


